

Roll No.

Total No. of Pages : 02

Total No. of Questions : 07

BCA (2011 & Onward) (Sem.-2)

COMPUTER SYSTEM ARCHITECTURE

Subject Code : BSBC-204

Paper ID : [B1116]

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTIONS TO CANDIDATES :

1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.**

SECTION-A

1. Write briefly :

- a) Define Instruction pipelining.
- b) Define MIMD.
- c) Give example of two address instruction.
- d) Name some Arithmetic micro operation.
- e) Define vector interrupt.
- f) What is strobe pulse?
- g) Define associative memory.
- h) How hit ratio is measured?
- i) Define address bus.
- j) What is memory stack?

SECTION-B

- 2) Explain stored program concept.
- 3) Discuss various types of addressing modes.
- 4) Explain various types of memory reference instructions.
- 5) Explain the working of 16 bit common bus system.
- 6) Explain the working of instruction cycle.
- 7) Discuss various types of memories.