

Roll No.

Total No. of Pages : 02

Total No. of Questions : 07

BCA (2011 & Onward) (Sem.-2)
COMPUTER SYSTEM ARCHITECTURE

Subject Code : BSBC-204

Paper ID : [B1116]

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTION TO CANDIDATES :

1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains SIX questions carrying TEN marks each and a student has to attempt any FOUR questions.**

SECTION-A

1. Write briefly :

- a) What are logical micro operations? Illustrate with examples.
- b) What is common bus system? List various types of buses.
- c) What is Instruction cycle?
- d) What is Interrupt? List its types.
- e) List various advantages of micro programmed control unit.
- f) What is memory mapped I/O?
- g) List different types of ports.
- h) What is hit ratio?
- i) Define memory hierarchy.
- j) List advantages of associative memory.

SECTION-B

2. What is Von Neumann Architecture? Explain Flynn's classification of computers.
3. What is micro operation? Explain various types of micro operations by taking suitable examples.
4. What is an instruction? Explain various instruction formats by taking suitable examples.
5. What is address mode? Explain the advantages and disadvantages of various addressing modes.
6. What are different techniques of data transfer? Discuss their relative merits and demerits.
7. What is cache memory? Explain how memory mapping is carried out in cache memory.