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Total No. of Pages : 02

Total No. of Questions : 07

BCA (Sem.-2)
COMPUTER SYSTEM ARCHITECTURE
Subject Code : UGCA-1908
M.Code : 77416
Date of Examination : 13-12-2024

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTIONS TO CANDIDATES :

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.

SECTION-A

1. Write briefly:

- a) What are the various types of Logic gates?
- b) Explain SOP and POS forms.
- c) What is Bus?
- d) What is Multiplexure?
- e) What is D flip flop, explain with example?
- f) What is Decoder?
- g) What is Race around condition in Flip flop?
- h) What is instruction format?
- i) What are I/O instructions?
- j) What is 16 bit common bus?

SECTION-B

2. Explain the implementation of AND, OR and NOT gate with universal gates.
3. Draw and explain Half Adder and Half subtracter with example.
4. Explain Implementation of Boolean equations with Demultiplexer.
5. Draw and explain JK Flip flop and D Flip flop.
6. What is stored program concept-Von Neumann Architecture? Explain.
7. Explain the three types of Buses in Computer architecture.

NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.